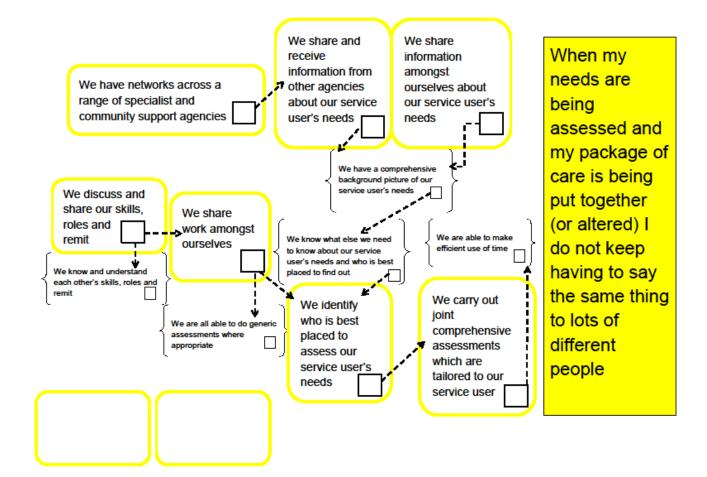
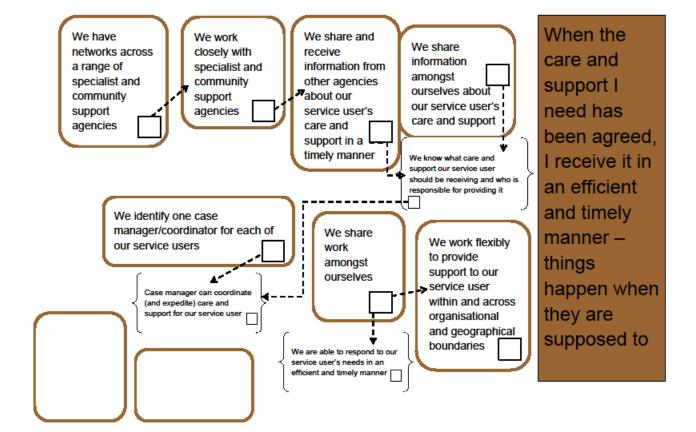
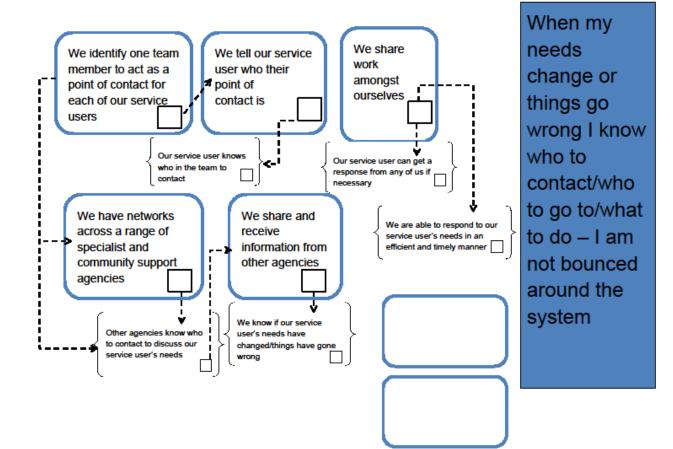
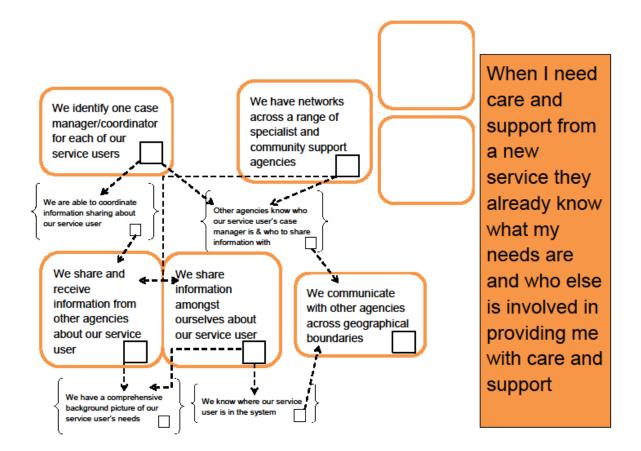
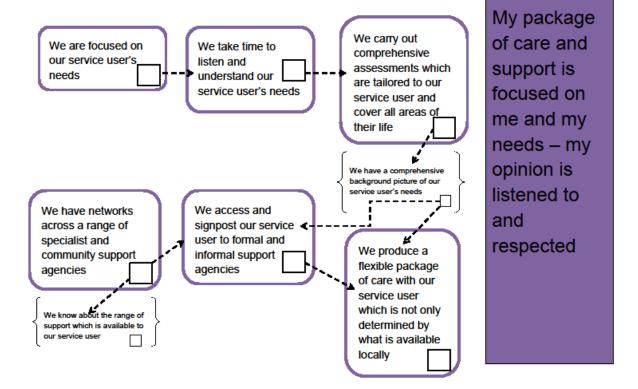
## **Appendix 1: SUFFICE logic models**

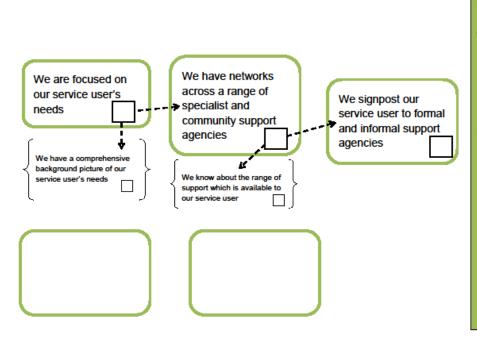












I know about the range of formal and informal support that is available to me